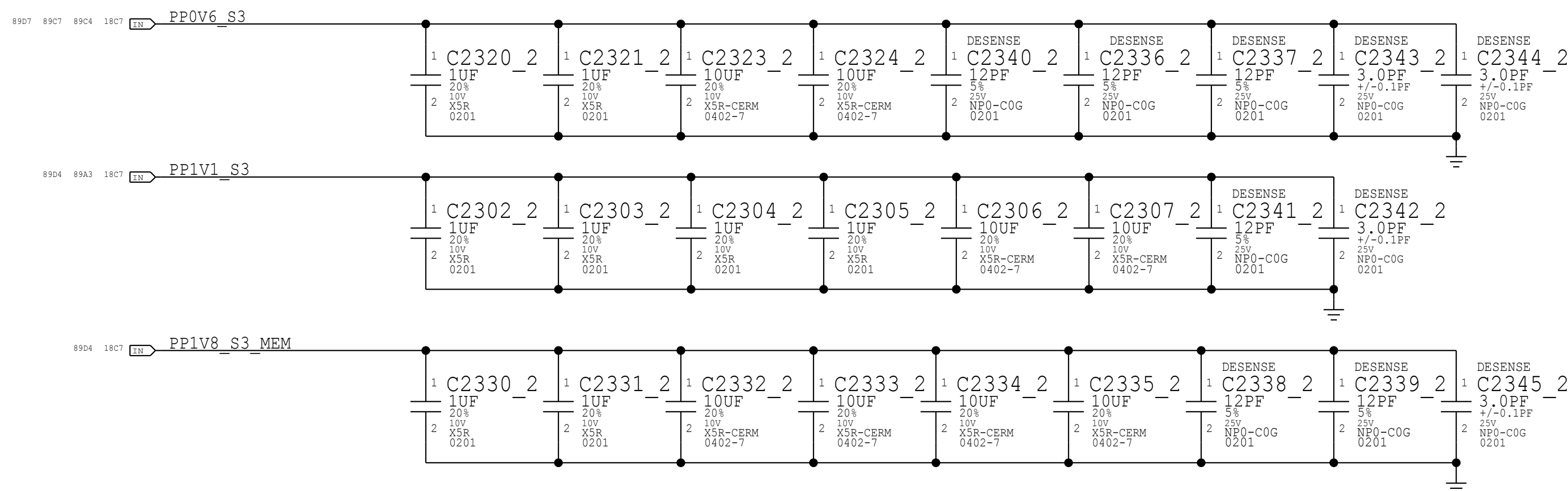
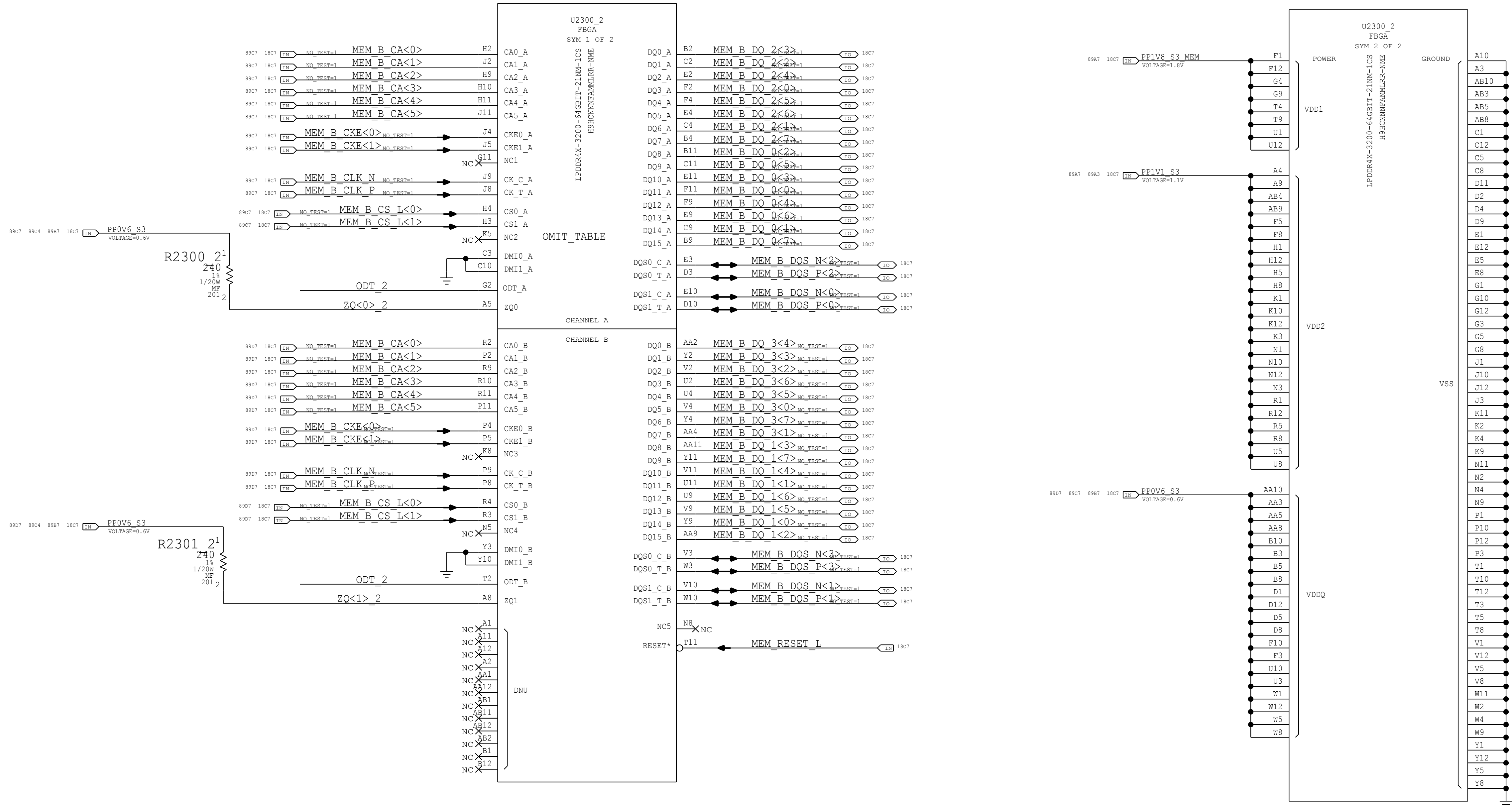
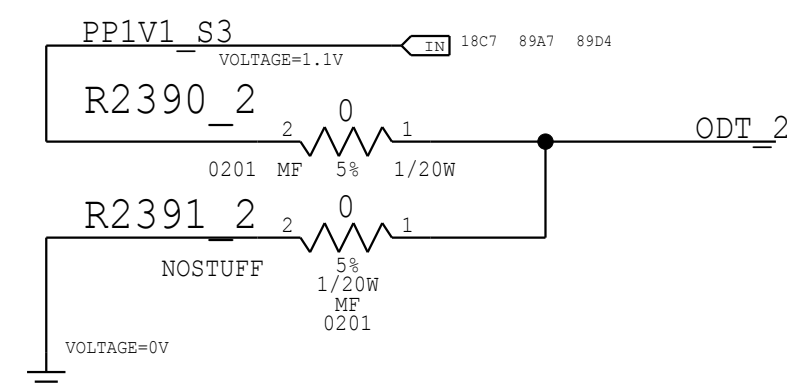



LPDDR4x SUB CHANNEL



H9HCNNFAMMLRR 3.3.23 NOTE 4: ODT CA PIN IGNORED BY LPDDR4x DEVICES, CONNECT TO VALID-LOGIC LEVEL EITHER VDD2 OR VSS



BOM_COST_GROUP=DRAM

SYNC MASTER=J79A, MLB		SYNC DATE=01/31/2017		
PAGE TITLE				
LPDDR4x Sub-Channel				
	Apple Inc.	DRAWING NUMBER	051-05198	SIZE
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